

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): LUSKY, Eli et al. EXAMINER: HO, H.
SERIAL NO.: Not Yet Assigned GROUP ART UNIT: 2818
FILED: Herewith Attorney Docket No.: P-4006-US1
FOR.: METHOD FOR ERASING A MEMORY CELL

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, this Information Disclosure Statement includes Form PTO-1449:

1. ☒ listing documents including patents, publications, and other information for consideration by the Examiner, copies of which are included with this information disclosure statement;
2. ☒ listing documents including patents, publications and other information that have been previously cited or submitted to the Patent Office in prior application U.S. Serial No. 09/983,510, filed October 24, 2001 which is properly identified and relied on for an earlier effective filing date under 35 U.S.C. 120 for consideration by the Examiner; however, in accordance with 37 C.F.R. 1.98(d), copies of such documents marked with an asterisk are not included in this information disclosure statement; and/or
3. ☒ listing other information for the Examiner's consideration which was cited in a communication from a foreign patent office in a counterpart foreign application, a copy of which is included with this information disclosure statement.

The information herein cited is only in fulfillment of Applicant(s) duty of candor in disclosing all information brought to Applicant(s) attention. This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art". If it should be determined that any of the

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Page 2

listed documents do not constitute "prior art" under United States law, Applicant(s) reserve the right to present to the office the relevant facts and law regarding the appropriate status of such documents.

Applicant(s) further reserve(s) the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

In accordance with MPEP Sections 609 and 707.05(b), it is requested that each and every document cited (including any cited in applicant's specification which is not repeated on the attached Form PTO-1449) be given thorough consideration and that it be cited of record in the prosecution history of the present application by initialing on Form PTO-1449. Such initialing is requested even if the Examiner does not consider it to be prior art for any reason, or even if the Examiner does not believe that the guidelines for citation have been fully complied with. This is requested so that each document becomes listed on the face of the patent issuing on the present application and is evidence that the Examiner has considered the document.

This Information Disclosure Statement is being filed:

I) ☒ Within three (3) months of filing the subject Application or entry of the subject Application into the national stage or before mailing of the first Office Action on the merits of the subject Application or a request for continued examination thereof, whichever event occurs last pursuant to of 37 C.F.R §1.97 (b); or

II) ☐ After the period specified in (I) but before the mailing date of either a final Official Action under 37 C.F.R §1.113 or a notice of allowance under 37 C.F.R §1.311 whichever occurs first and;


1. ☐ the undersigned hereby states that each item of information listed on the Form PTO-1449 was either (i) cited in a communication from a foreign patent office in a counterpart foreign application not more than three (3) months prior to the filing of this Information Disclosure Statement or (ii) not cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, not known to any individual designated in §1.56(c) more than three (3) months prior to the filing of this information disclosure statement; or

2. ☐ the undersigned hereby authorizes the Patent Office to charge the fee in the amount of \$180.00 under 37 C.F.R §1.17 (p) to Deposit Account 05-0649.

- III) ☐ After the period in (I) and (II) but before the payment of the issue fee and,
1. The undersigned hereby states:
 - a) ☐ that each item of information cited on the form PTO-1449 was cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three (3) months prior to the filing of this Information Disclosure Statement; or
 - b) ☐ that no items of information contained in Form PTO-1449 was cited in a communication from a foreign patent office in a counterpart foreign application, and to the knowledge of the undersigned after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement; and
 2. ☐ The undersigned hereby authorizes the Patent Office to charge the Petition fee in the Amount of \$180.00 under 37 C.F.R §1.17 (p) to Deposit Account 05-0649.

Except for issue fees payable under 37 C.F.R. §1.18, the Commissioner is hereby authorized by this paper to charge any additional fees during the entire pendency of this application including fees due under 37 C.F.R. §§1.16 and 1.17 which may be required, including any required extension of time fees, or credit any overpayment to Deposit Account No. 05-0649.

Respectfully submitted,



Vladimir Sherman
Attorney for Applicant(s)
Registration No. 43,116

Dated: September 3, 2003

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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	Not Yet Assigned
				Filing Date	Herewith
				First Named Inventor	LUSKY, Eli
				Group Art Unit	2818
				Examiner Name	HO, H.
Sheet	1	of	8	Attorney Docket Number	P-4006-US1

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	AA*	3,895,360		Cricchi et al.	7/15/1975	
	AB*	4,016,588		Ohya et al.	4/5/1977	
	AC*	4,017,888		Christie et al.	4/12/1977	
	AD*	4,151,021		McElroy	4/24/1979	
	AE*	4,173,766		Hayes	11/6/1979	
	AF*	4,173,791		Bell	11/6/1979	
	AG*	4,281,397		Neal et al.	7/28/1981	
	AH*	4,306,353		Jacobs et al.	12/22/1981	
	AI*	4,342,149		Jacobs et al.	8/3/1982	
	AJ*	4,360,900		Bate	11/23/1982	
	AK*	4,380,057		Kotecha et al.	4/12/1983	
	AL*	4,388,705		Sheppard	6/14/1983	
	AM*	4,389,705		Sheppard	6/21/1983	
	AN*	4,471,373		Shimizu et al.	9/11/1984	
	AO*	4,521,796		Rajkanan et al.	6/4/1985	
	AP*	4,527,257		Cricchi	7/2/1985	
	AQ*	4,586,163		Koike	4/29/1986	
	AR*	4,630,085		Koyama	12/16/1986	
	AS*	4,667,217		Janning	5/19/1987	
	AT*	4,742,491		Liang et al.	5/3/1988	
	AU*	4,780,424		Holler et al.	10/25/1988	
	AV*	4,847,808		Kobatake	7/11/1989	
	AW*	4,870,470		Bass Jr. et al.	9/26/1989	
	AX*	4,916,671		Ichiguchi	4/10/1990	
	AY*	4,941,028		Chen et al.	7/10/1990	
	AZ*	5,021,999		Kohda et al.	6/4/1991	
	BA*	5,075,245		Woo et al.	12/24/1991	
	BB*	5,117,389		Yiu	5/26/1992	
	BC*	5,159,570		Mitchell et al.	10/27/1992	
	BD*	5,168,334		Mitchell et al.	12/1/1992	
	BE*	5,172,338		Mehrotra et al.	12/15/1992	
	BF*	5,175,120		Lee	12/29/1992	
	BG*	5,214,303		Aoki	5/25/1993	
	BH*	5,241,497		Komarek	8/31/1993	

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				First Named Inventor	LUSKY, Eli
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		Number	Kind Code ² (if known)			
	BI*	5,260,593		Lee	11/9/1993	
	BJ*	5,268,861		Hotta	12/7/1993	
	BK*	5,289,412		Frary et al.	2/22/1994	
	BL*	5,293,563		Ohta	3/8/1994	
	BM*	5,295,108		Higa	3/15/1994	
	BN*	5,305,262		Yoneda	4/19/1994	
	BO*	5,311,049		Tsuruta	5/10/1994	
	BP*	5,315,541		Harari et al.	5/24/1994	
	BQ*	5,338,954		Shimoji	8/16/1994	
	BR*	5,345,425		Shikatani	9/6/1994	
	BS*	5,349,221		Shimoji	9/20/1994	
	BT*	5,350,710		Hong et al.	9/27/1994	
	BU*	5,359,554		Odake et al.	10/25/1994	
	BV*	5,393,701		Ko et al.	2/28/1995	
	BW*	5,394,355		Uramoto et al.	2/28/1995	
	BX*	5,399,891		Yiu et al.	3/21/1995	
	BY*	5,412,601		Sawada et al.	5/2/1995	
	BZ*	5,414,693		Ma et al.	5/2/1995	
	CA*	5,418,176		Yang et al.	5/23/1995	
	CB*	5,418,743		Tomioka et al.	5/23/1995	
	CC*	5,422,844		Wolstenholme et al.	6/6/1995	
	CD*	5,424,978		Wada et al.	6/13/1995	
	CE*	5,426,605		Van Berkel et al.	6/20/1995	
	CF*	5,434,825		Harari	7/18/1995	
	CG*	5,440,505		Fazio et al.	8/8/1995	
	CH*	5,450,341		Sawada et al.	9/12/1995	
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	CJ*	5,455,793		Amin et al.	10/3/1995	
	CK*	5,467,308		Chang et al.	11/14/1995	
	CL*	5,477,499		Van Buskirk et al.	12/19/1995	
	CM*	5,495,440		Asakura	2/27/1996	
	CN*	5,518,942		Shrivastava	5/21/1996	
	CO*	5,521,870		Ishikawa	5/28/1996	

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		Number	Kind Code ² (if known)			
	CP*	5,523,251		Hong	6/4/1996	
	CQ*	5,523,972		Rashid et al.	6/4/1996	
	CR*	5,553,018		Wang et al.	9/3/1996	
	CS*	5,563,823		Yiu et al.	10/8/1996	
	CT*	5,583,808		Brahmbhatt	12/10/1996	
	CU*	5,599,727		Hakozaki et al.	2/4/1997	
	CV*	5,623,438		Guritz et al.	4/22/1997	
	CW*	5,654,568		Nakao	8/5/1997	
	CX*	5,656,513		Wang et al.	8/12/1997	
	CY*	5,661,060		Gill et al.	8/26/1997	
	CZ*	5,683,925		Irani et al.	11/4/1997	
	DA*	5,712,814		Fratin et al.	1/27/1998	
	DB	5,715,193		Norman	2/3/1998	
	DC*	5,726,946		Yamagata et al.	3/10/1998	
	DD*	5,751,037		Aozasa et al.	5/12/1998	
	DE*	5,754,475		Bill et al.	5/19/1998	
	DF*	5,768,192		Eitan	6/16/1998	
	DG	5,768,193		Lee et al.	6/16/1998	
	DH*	5,777,919		Chi-Yung et al.	7/7/1998	
	DI*	5,784,314		Sali et al.	7/21/1998	
	DJ*	5,787,036		Okazawa	7/28/1998	
	DK*	5,793,079		Georgescu et al.	8/11/1998	
	DL*	5,812,449		Song	9/22/1998	
	DM*	5,825,686		Schmitt-Landsiedel et al.	10/20/1998	
	DN*	5,834,851		Ikeda et al.	11/10/1998	
	DO*	5,836,772		Chang et al.	11/17/1998	
	DP*	5,841,700		Chang	11/24/1998	
	QQ*	5,847,441		Cutter et al.	12/8/1998	
	DR*	5,862,076		Eitan	1/19/1999	
	DS*	5,864,164		Wen	1/26/1999	
	DT*	5,870,335		Khan et al.	2/9/1999	
	DU*	5,886,927		Takeuchi	3/23/1999	
	DV*	5,946,558		Hsu	8/31/1999	
	DW*	5,949,728		Liu et al.	9/7/1999	

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Application Number		Not Yet Assigned			
Filing Date		Herewith			
First Named Inventor		LUSKY, Eli			
Group Art Unit		2818			
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		Number	Kind Code ² (if known)			
	DX*	5,963,412		En	10/5/1999	
	DY*	5,963,465		Eitan	10/5/1999	
	DZ	5,946,258		Evertt et al.	8/31/1999	
	EA	5,969,993		Takeshima	10/19/1999	
	EB*	5,973,373		Krauschneider et al.	10/26/1999	
	EC*	5,990,526		Bez et al.	11/23/1999	
	ED*	5,991,202		Derhacopian et al.	11/23/1999	
	EE*	6,011,725		Eitan	1/4/2000	
	EF*	6,018,186		Hsu	1/25/2000	
	EG*	6,020,241		You et al.	2/1/2000	
	EH*	6,028,324		Su et al.	2/22/2000	
	EI*	6,030,871		Eitan	2/29/2000	
	EJ*	6,034,403		Wu	3/7/2000	
	EK	6,034,896		Ranaweera et al.	3/7/2000	
	EL*	6,063,666		Chang et al.	5/16/2000	
	EM*	6,097,639		Choi et al.	8/1/2000	
	EN*	6,128,226		Eitan et al.	10/3/2000	
	EO*	6,134,156		Eitan	10/17/2000	
	EP*	6,137,718		Reisinger	10/24/2000	
	EQ	6,157,570		Nachumovsky	12/5/2000	
	ER*	6,163,048		Hirose et al.	12/19/2000	
	ES	6,181,605		Hollmer et al.	1/30/2001	
	ET*	6,201,282		Eitan	3/2001	
	EU*	6,215,148		Eitan	4/10/2001	
	EV	6,215,702		Derhacopian et al.	4/10/2001	
	EW	6,256,231		Lavi et al.	7/3/2001	
	EX	6,266,281		Derhacopian et al.	7/24/2001	
	EY*	6,285,574		Eitan	9/4/2001	
	EZ*	6,292,394		Cohen et al.	9/18/2001	
	FA*	6,304,485		Harari et al.	10/16/2001	
	FB*	6,348,711		Eitan	2/19/2002	
	FC*	6,396,741		Bloom et al.	5/28/2002	
	FD	6,490,204		Bloom et al.	12/3/2002	
	FE	2002/0132436		Eliyahu et al.	9/19/2002	

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (where appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	GD*	Chan et al., " <u>A True Single-Transistor Oxide-Nitride-Oxide EEPROM Device</u> ," IEEE Electron Device Letters, vol. EDL-8, No. 3, Mar. 1987.	<input type="checkbox"/>
	GE*	Eitan et al., " <u>Hot-Electron Injection into the Oxide in n-Channel MOS Devices</u> ," IEEE Transactions on Electron Devices, vol. ED-38, No. 3, pp. 328-340, Mar. 1981.	<input type="checkbox"/>
	GF*	Roy, Anirban " <u>Characterization and Modeling of Charge Trapping and Retention in Novel Multi-Dielectric Nonvolatile Semiconductor Memory Device</u> ," Doctoral Dissertation, Sherman Fairchild Center, Department of Computer Science and Electrical Engineering, pp. 1-35, 1989.	<input type="checkbox"/>
	GG*	" <u>2 Bit/Cell EEPROM Cell Using Band-To-Band Tunneling For Data Read-Out</u> ," IBM Technical Disclosure Bulletin, US IBM Corp. NY Vol. 35, No 4B, ISSN: 0018-88689, September, 1992.	<input type="checkbox"/>
	GH*	Hsing-Huang Tsent et al. " <u>Thin CVD Gate Dielectric for USLI Technology</u> ," IEEE, 0-7803-1450-6, 1993.	<input type="checkbox"/>
	GI*	Pickar, K.A., " <u>Ion Implementation in Silicon</u> ," Applied Solid State Science, Vol. 5, R. Wolfe Edition, Academic Press, New York, 1975.	<input type="checkbox"/>
	GJ*	Bruno Ricco, " <u>Nonvolatile Multilevel Memories for Digital Application</u> ," IEEE, vol. 86, No. 12, issued Dec. 1998, pp. 2399-2421.	<input type="checkbox"/>
	GK*	Chang, J., " <u>Non Volatile Semiconductor Memory Devices</u> ," Proceeding of the IEEE, Vol. 64 No. 7, Issued July 1976.	<input type="checkbox"/>
	GL*	Ma et al., " <u>A dual-bit Split-Gate EEPROM (DSG) Cell in Contactless Array for Single-Vcc High Density Flash Memories</u> ," IEEE, pp.3.5.1-3.5.4, 1994.	<input type="checkbox"/>
	GM*	Oshima et al., " <u>Process and Device Technologies for 16Mbit Eproms with Large – Tilt – Angle Implemented P-Pocket Cell</u> ," IEEE, CH2865-4/90/0000-0095, pp. 5.2.1 – 5.2.4, 1990.	<input type="checkbox"/>
	GN*	Lee, H., " <u>A New Approach For the Floating-Gate MOS NonVolatile Memory</u> ," Applied Physics Letters, Vol. 31, No. 7, pages 475-6, October 1977.	<input type="checkbox"/>
	GO*	Bhattacharyya et al., " <u>FET Gate Structure for Nonvolatile N-Channel Read-Mostly Memory Devices</u> ," IBM Technical Disclosure Bulletin, US IBM Corp. Vol. 18, No. 6, page 1768, 1976.	<input type="checkbox"/>

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		Application Number	Not Yet Assigned		
		Filing Date	Herewith		
		First Named Inventor	LUSKY, Eli		
		Group Art Unit	2818		
		Examiner Name	HO, H.		
Sheet	8	of	8	Attorney Docket Number	P-4006-US1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (where appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	GP*	Bude et al., "EEPROM/Flash Sub 3.0V Drain-Source Bias Hot Carrier Writing", IEDM 95, pp. 989-992.	<input type="checkbox"/>
	GQ*	Bude et al., "Secondary Electron Flash – a High Performance, Low Power Flash Technology for 0.35 µm and Below", IEDM 97, pp. 279-282.	<input type="checkbox"/>
	GR*	Bude et al., "Modeling Nonequilibrium Hot Carrier Device Effects", Conferences of Insulator Specialists of Europe, Sweden, June, 1997.	<input type="checkbox"/>
	GS*	Eitan, US Patent Application No. 08/905,286, filed August 1, 1997	<input type="checkbox"/>
	GT*	Eitan, US Patent Application No. 09/536,125, filed March 28, 2000	<input type="checkbox"/>
	GU*	Avni et al., US Patent Application No. 09/730,586, filed December 7, 2000	<input type="checkbox"/>
	GV*	Eitan, US Patent Application No. 08/902,890, filed July 30, 1997	<input type="checkbox"/>
	GW	International Search Report of International Application PCT/IL02/00855 dated April 21, 2003	<input type="checkbox"/>
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